

Abstract Machines

Programming Language Habitats

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CSCI 305

What is an Abstract Machine?

- An **abstract machine** is the abstraction of a physical computing machine
- A **programming language** \mathcal{L} provides instructions used to construct programs
- A **program** written in \mathcal{L} is described in terms of those instructions
- A language \mathcal{L} recognized by $\mathcal{M}_{\mathcal{L}}$ is called the **machine language** of $\mathcal{M}_{\mathcal{L}}$

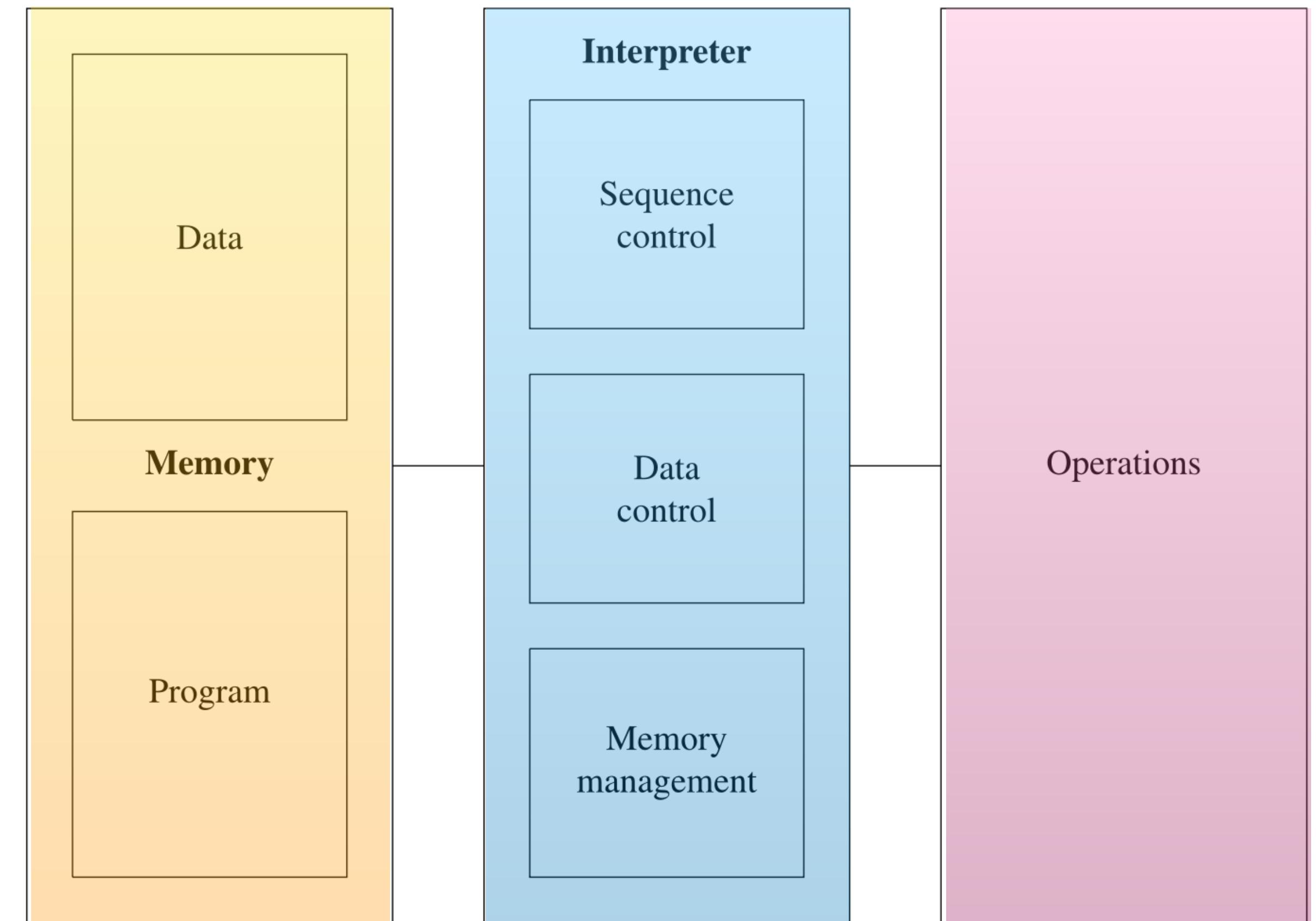
Abstract Machine

Given a programming language \mathcal{L} , an **abstract machine** for \mathcal{L} is denoted by $\mathcal{M}_{\mathcal{L}}$.

Any set of data structures and algorithms which can perform the storage and execution of written programs in the language \mathcal{L} is an $\mathcal{M}_{\mathcal{L}}$.

What is an Abstract Machine?

- A generic abstract machine $\mathcal{M}_{\mathcal{L}}$ is composed of a **store** and an **interpreter**
- The **store** contains programs and data
- The **interpreter** executes the instructions contained in programs
- Standard **operations** common to all interpreters



Operations

- Four categories of operations:
 1. Processing primitive data
 2. Control flow
 3. Data transfer
 4. Memory management

Basic arithmetic for integers and floating point numbers.
Boolean logic.

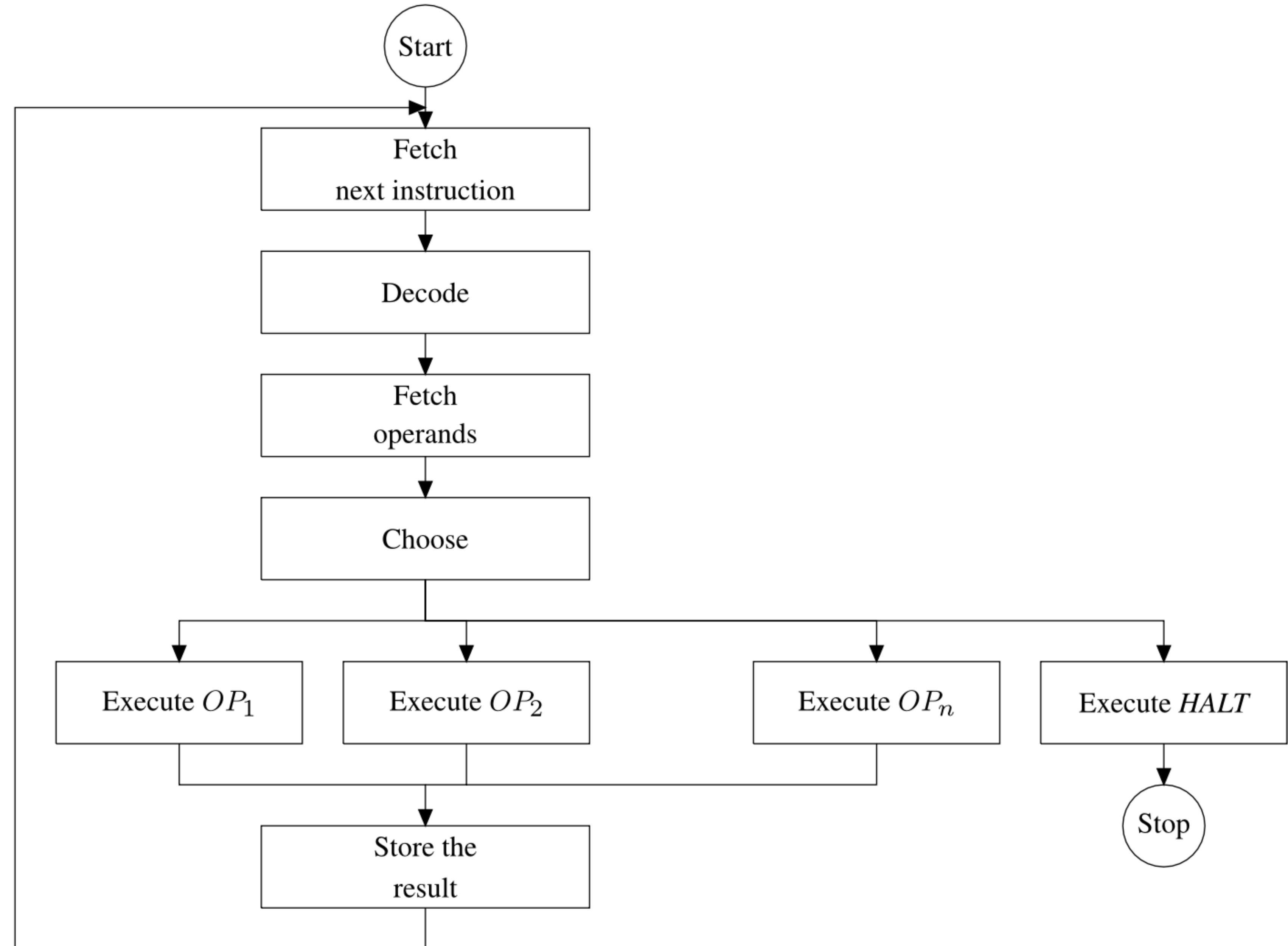
Keep track of the next instruction to execute.
Support for conditional execution.

Store programs and associated data in memory.
Support dynamic allocation of memory.

Move data between memory and registers.
What data should be available for instructions.

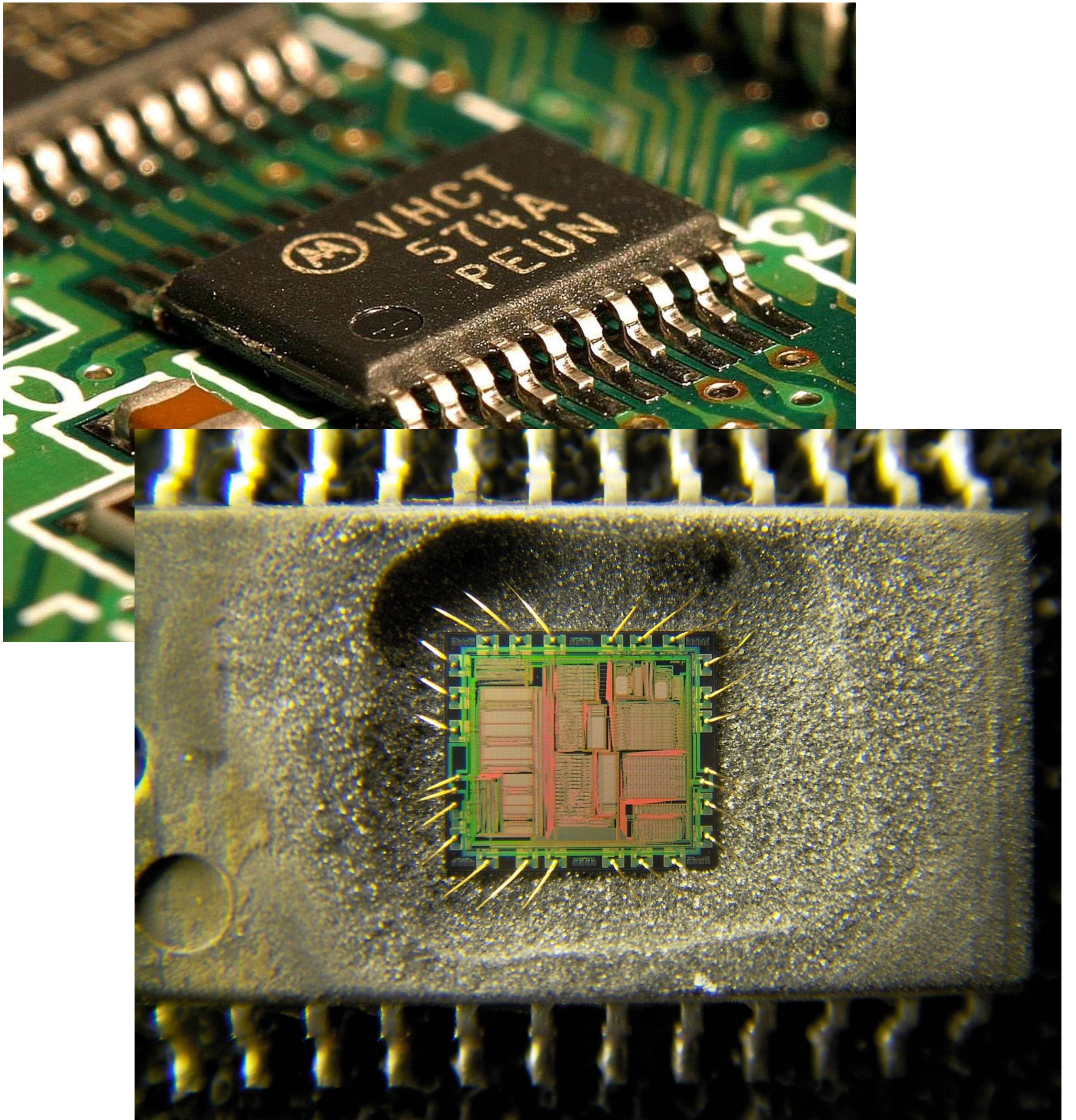
Interpreter Execution Cycle

- Fetch next instruction from memory
- Fetch operands
- Execute the instruction
- Store the result



Hardware Machine

- Implemented using logic circuits and electronic components
- Typically supports simple operations
 - ADD reg1, reg2
- CPU executing instructions can be viewed as an interpreter



Implementation of an Abstract Machine

- Three options:
 - Implementation in hardware
 - ▶ Build custom hardware for a language
 - Simulation using software
 - ▶ Write an interpreter
 - Emulation using firmware
 - ▶ Use a general-purpose CPU

Pros

- Fast

Cons

- Complicated
- Expensive
- Cannot be modified

Pros

- Flexible

Cons

- Slow

Pros

- Reasonably Fast
- Reasonably Flexible

Cons

- Not as fast or as flexible as other options

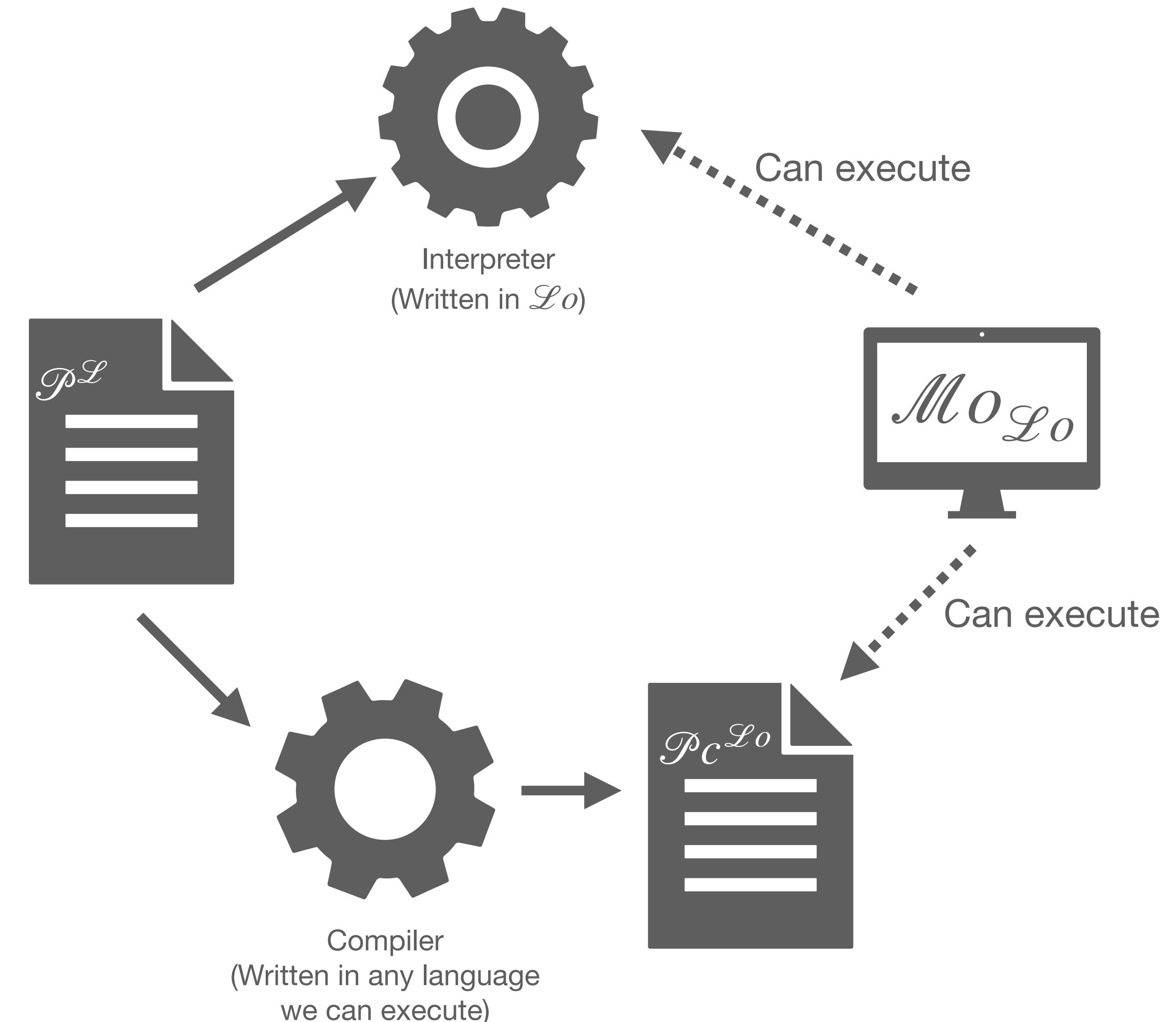
Programs

- A program written in \mathcal{L} can be seen as a ***partial function*** for generic data \mathcal{D} :
 - $\mathcal{P}^{\mathcal{L}} : \mathcal{D} \rightarrow \mathcal{D}$
 - i.e., $\mathcal{P}^{\mathcal{L}}(\text{Input}) = \text{Output}$
- The superscript \mathcal{L} indicates that a program (e.g., $\mathcal{P}^{\mathcal{L}}$) is written in language \mathcal{L}
- The subscript \mathcal{L} indicates a construct (e.g., $\mathcal{M}_{\mathcal{L}}$) is used for a language \mathcal{L}

Implementation

The Ideal Case

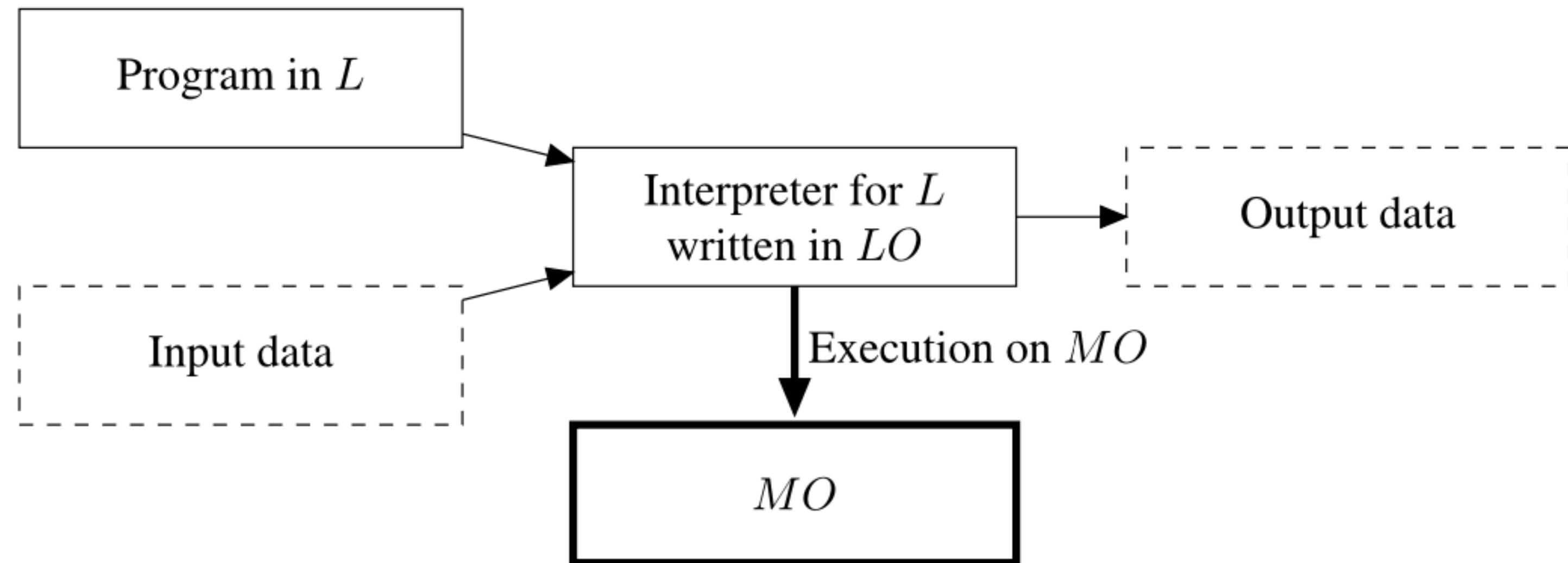
- Consider a generic programming language \mathcal{L} we want to implement
- We don't have an abstract machine $\mathcal{M}_{\mathcal{L}}$
- If we have a **host machine**, $\mathcal{M}_{\mathcal{O}_{\mathcal{L}o}}$, we can run programs written in \mathcal{L}
 - **Purely interpreted implementation**
 - **Purely compiled implementation**



Purely Interpreted

The Ideal Form

- Interpreter for $\mathcal{M}_{\mathcal{L}}$ is a program implemented in the language \mathcal{L}_0
- The interpreter that can execute a program $\mathcal{P}^{\mathcal{L}}$ (written in \mathcal{L})
- In a purely interpreted impl., programs in \mathcal{L} are not explicitly translated
- Execution happens by using instructions in \mathcal{L}_0 that correspond to an instruction in \mathcal{L}



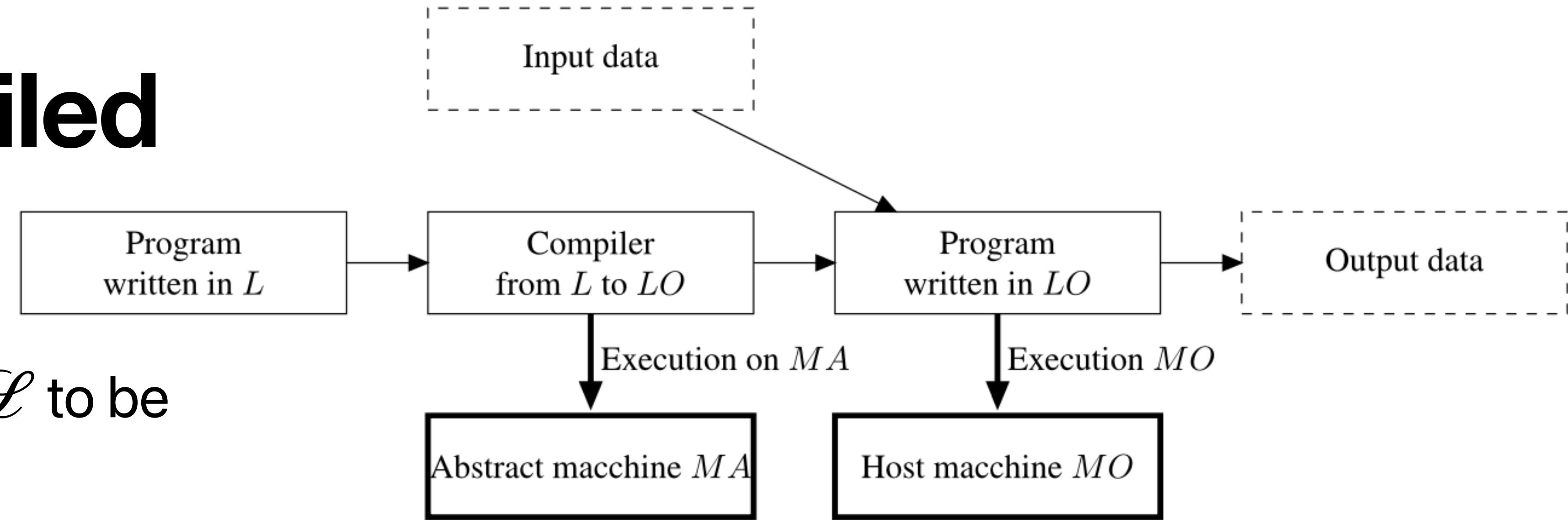
$$\mathcal{I}_{\mathcal{L}}^{\mathcal{L}_0} : (\mathcal{P}^{\mathcal{L}} \times \mathcal{D}) \rightarrow \mathcal{D}$$

such that, $\mathcal{I}_{\mathcal{L}}^{\mathcal{L}_0}(\mathcal{P}^{\mathcal{L}}, \text{Input}) = \mathcal{P}^{\mathcal{L}}(\text{Input})$

Purely Compiled

The Ideal Form

- Translate programs in \mathcal{L} to be programs in \mathcal{L}_o
- A program called a *compiler* $\mathcal{C}_{\mathcal{L}, \mathcal{L}_o}$ accepts source *language* programs (in \mathcal{L}) and translates them to object *language* programs (in \mathcal{L}_o)
- The compiled program $\mathcal{P}_{\mathcal{C}}^{\mathcal{L}_o}$ can be run directly on the host machine $\mathcal{M}_{\mathcal{O}_{\mathcal{L}_o}}$


$$\mathcal{C}_{\mathcal{L}, \mathcal{L}_o} : \mathcal{P}^{\mathcal{L}} \rightarrow \mathcal{P}^{\mathcal{L}_o}$$

such that, given a program $\mathcal{P}^{\mathcal{L}}$, if

$$\mathcal{C}_{\mathcal{L}, \mathcal{L}_o}(\mathcal{P}^{\mathcal{L}}) = \mathcal{P}_{\mathcal{C}}^{\mathcal{L}_o},$$

then, for every Input $\in \mathcal{D}$:

$$\mathcal{P}^{\mathcal{L}}(\text{Input}) = \mathcal{P}_{\mathcal{C}}^{\mathcal{L}_o}(\text{Input})$$

Comparison

Interpreted vs Compiled

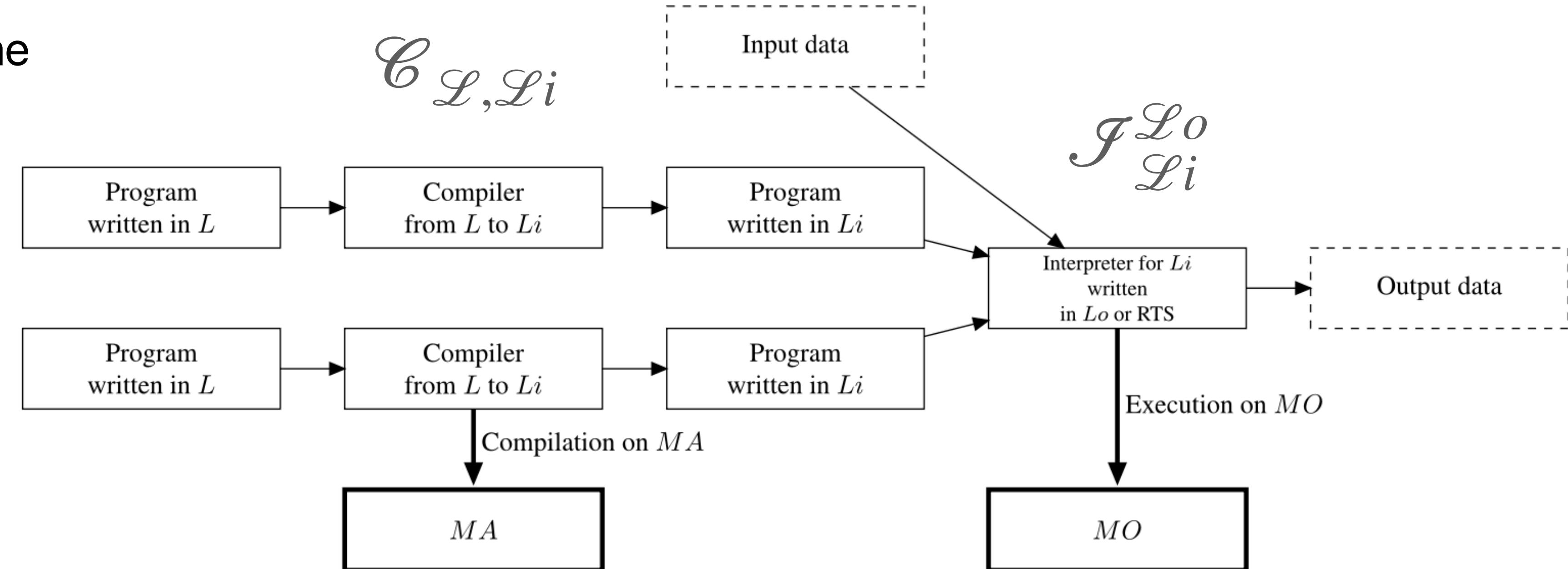
- Purely interpreted
 - Inefficient
 - ▶ E.g., parsing must happen during interpretation
 - Flexibility
 - ▶ Can easily change the semantics of the language or modify implementation to better support tools/debugging
 - Simpler
 - ▶ Often, but not always, an interpreter is simpler to build
- Purely compiled
 - Efficient (at runtime)
 - ▶ Compiled program executes natively on $\mathcal{M}_{\mathcal{L}_0}$
 - Information loss
 - ▶ Details of the source program are lost
 - ▶ E.g., identity of variables in a function
 - More complex
 - ▶ Often, but not always, a compiler is more complex to build



Intermediate Machine

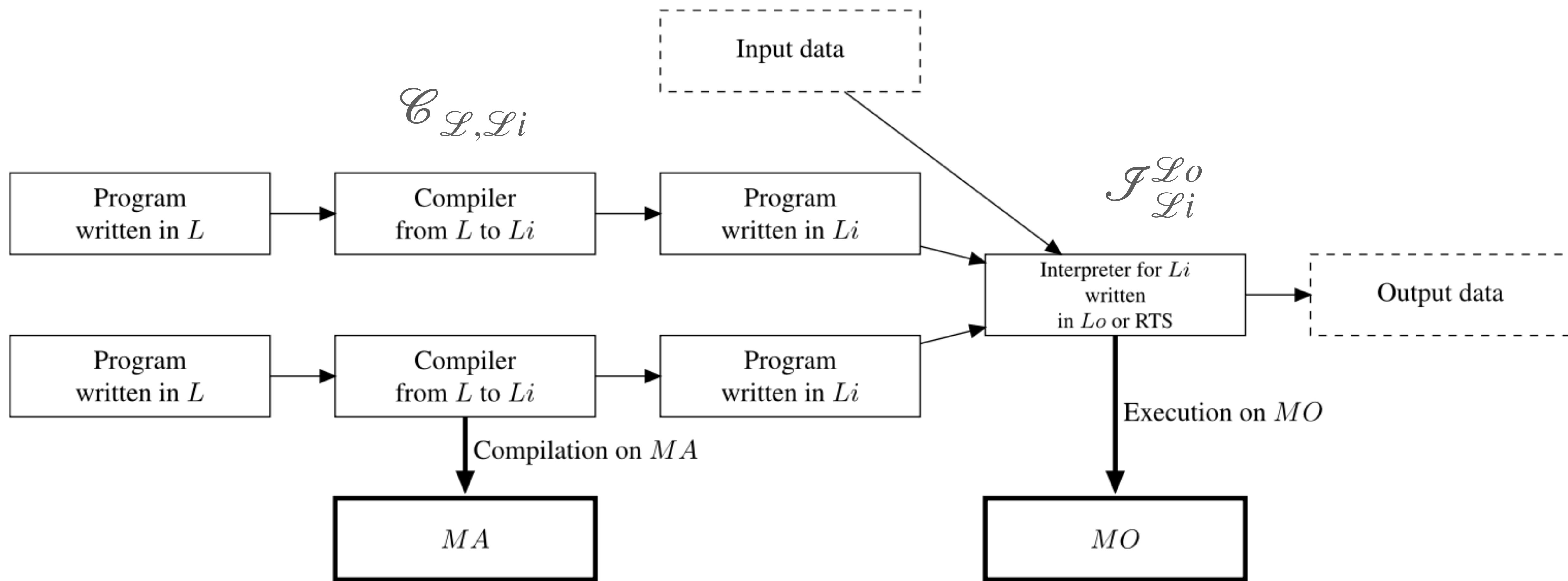
The Real Case

- Purely compiled and purely implemented are the extreme cases
- All real interpreters operate on an intermediate representation (IR)
- All real compiled programs use some simulated constructs
- \mathcal{L}_i and $\mathcal{M}_{\mathcal{L}_i}$ are the intermediate language and machine



Intermediate Machine

The Real Case



Hierarchies of Abstract Machines



- Each level implements an abstract machine with its own language
- We are constantly implementing abstract machines
 - A new program can be viewed as an abstract machine
- Software all the way down...until we hit hardware

